

# SANYO Semiconductors DATA SHEET

# 2SK4183 — General-Purpose Switching Device Applications

# **Features**

- · Low ON-resistance, low input capacitance, ultrahigh-speed switching.
- · Adoption of high reliability HVP process.
- · Avalanche resistance guarantee.
- · For use of lighting.

# **Specifications**

#### Absolute Maximum Ratings at Ta=25°C

Parameter	Symbol	Conditions	Ratings	Unit
Drain-to-Source Voltage	VDSS		525	V
Gate-to-Source Voltage	VGSS		±30	V
Drain Current (DC)	ID		13	Α
Drain Current (Pulse)	IDP	PW≤10μs, duty cycle≤1%	52	Α
Allowable Power Dissipation	PD	Tc=25°C (SANYO's ideal heat dissipation condition*1)	100	W
Channel Temperature	Tch		150	°C
Storage Temperature	Tstg		-55 to +150	°C
Avalanche Energy (Single pulse) *2	EAS		103	mJ
Avalanche Current *3	IAV		13	А

<sup>\*1</sup> SANYO's condition is radiation from backside.

The method is applying silicone grease to the backside of the device and attaching the device to water-cooled radiator made of aluminium.

Marking: K4183

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<sup>\*2</sup> V<sub>DD</sub>=99V, L=1mH, I<sub>A</sub>V=13A

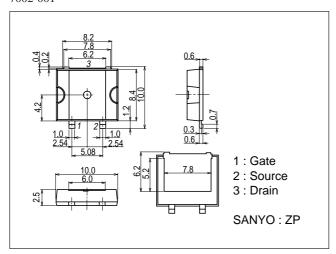
<sup>\*3</sup> L≤1mH, single pulse

# Electrical Characteristics at Ta=25°C

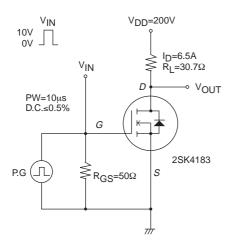
Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	Offic
Drain-to-Source Breakdown Voltage	V(BR)DSS	ID=10mA, VGS=0V	525			V
Zero-Gate Voltage Drain Current	IDSS	V <sub>DS</sub> =420V, V <sub>GS</sub> =0V			100	μΑ
Gate-to-Source Leakage Current	IGSS	V <sub>GS</sub> =±30V, V <sub>DS</sub> =0V			±100	nA
Cutoff Voltage	VGS(off)	VDS=10V, ID=1mA	3		5	V
Forward Transfer Admittance	yfs	V <sub>DS</sub> =10V, I <sub>D</sub> =6.5A	3.5	7.0		S
Static Drain-to-Source On-State Resistance	R <sub>DS</sub> (on)	I <sub>D</sub> =6.5A, V <sub>GS</sub> =10V		0.45	0.58	Ω
Input Capacitance	Ciss	V <sub>DS</sub> =30V, f=1MHz		1000		pF
Output Capacitance	Coss	V <sub>DS</sub> =30V, f=1MHz		195		pF
Reverse Transfer Capacitance	Crss	V <sub>DS</sub> =30V, f=1MHz		44		pF
Turn-ON Delay Time	t <sub>d</sub> (on)	See specified Test Circuit.		26		ns
Rise Time	t <sub>r</sub>	See specified Test Circuit.		101		ns
Turn-OFF Delay Time	t <sub>d</sub> (off)	See specified Test Circuit.		115		ns
Fall Time	tf	See specified Test Circuit.		56		ns
Total Gate Charge	Qg	V <sub>DS</sub> =200V, V <sub>GS</sub> =10V, I <sub>D</sub> =13A		38.2		nC
Gate-to-Source Charge	Qgs	V <sub>DS</sub> =200V, V <sub>GS</sub> =10V, I <sub>D</sub> =13A		6.7		nC
Gate-to-Drain "Miller" Charge	Qgd	V <sub>DS</sub> =200V, V <sub>GS</sub> =10V, I <sub>D</sub> =13A		21.5		nC
Diode Forward Voltage	V <sub>SD</sub>	I <sub>S</sub> =13A, V <sub>GS</sub> =0V		0.9	1.2	V

# **Package Dimensions**

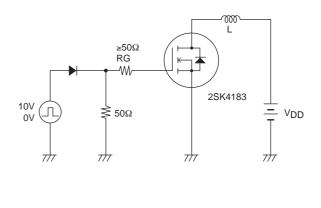
unit : mm (typ) 7002-001

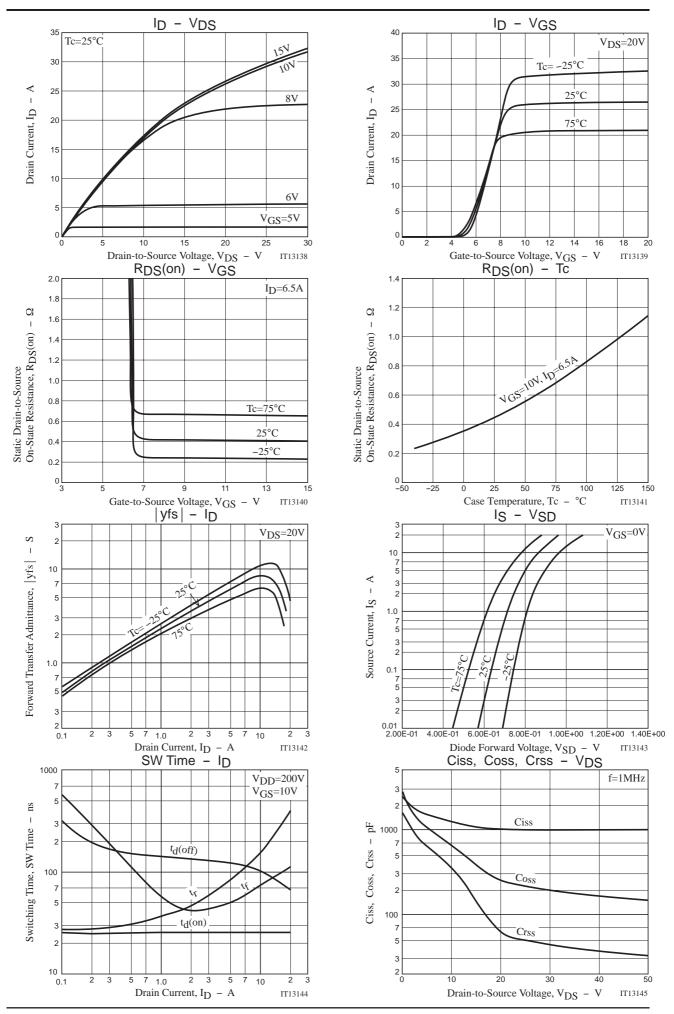


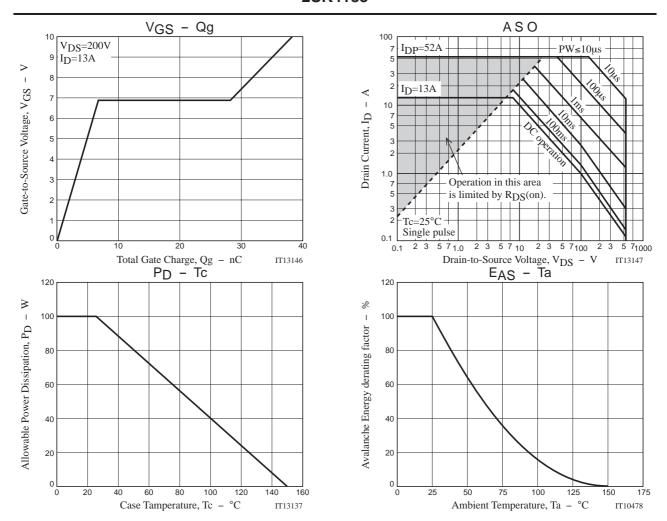
# **Switching Time Test Circuit**



# **Avalanche Resistance Test Circuit**







Note on usage: Since the 2SK4183 is a MOSFET product, please avoid using this device in the vicinity of highly charged objects.

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